

## TAB HALL OF HONOR RECIPIENT NOMINATION FORM



### **CANDIDATE NAME Rakesh Kumar**

**BIOGRAPHY:** Rakesh Kumar is a 43-year semiconductor industry veteran, an entrepreneur, and now an educator. He has been an industry individual contributor, technology leader and a manager. He has been a consulting adviser to numerous emerging, mid-size and large Fortune 500 companies, and leading research organizations. Dr. Kumar authored "*Fabless Semiconductor Implementation*", a book published by McGraw Hill. His experience is leveraged in a very popular course at UC San Diego that educates, encourages and coaches students

about successful entrepreneurship. He is an IEEE Life Fellow and a volunteer for 25+ years. Dr. Kumar was the VP&GM of Cadence Design's worldwide Silicon Technology Services business, and held various technical and management positions at Unisys and Motorola. He received the Ph.D. and M.S. EE from the University of Rochester in 1974 and 1971 respectively, the BTechEE from IIT Delhi in 1969, and an Executive MBA from the UC, San Diego in 1989.

### **CITATION:**

**For leading and growing TAB's participation Sections Congresses.**

### **DESCRIPTION OF ACCOMPLISHMENT(S) RELATIVE TO MAJOR CONTRIBUTION(S) TO IEEE TECHNICAL ACTIVITIES**

TAB is interested in having a significant role in Section Congress, to ensure attendees are exposed to Technical Activities (TA) and to encourage strong collaboration between MGA and TA. Rakesh Kumar led the planning for these activities starting with the 2011 Sections Congress, resulted in a successful event with substantial TA content and participation. Rakesh again led the TAB participation in the 2014 and 2017 Sections Congresses, in both cases spearheading greater TA contributions and participation than before. Rakesh lead and coordinated the efforts of a diverse set of volunteers and staff in TA and MGA to ensure coordinated activities and successful outcomes.

### **DATE(S) OF ACCOMPLISHMENT(S):**

May 2010-August 2011

May 2013-August 2014

May 2016-August 2017

### **RELEVANT IEEE ACTIVITIES:**

#### TECHNICAL ACTIVITIES BOARD:

Chair, Adhoc supporting Sections Congress 2017, 2014, 2011

Member 2012-13, alternate 2006-11

Management Committee 2015-17

Strategic Planning 2014-16

Member, Awards Recommendation Committee 2017

Chair, SSIT Turnaround Adhoc 2012-14

Representative, MGA Ops 2011

Ethics and Conflict Resolution 2010

FinCom 2007-09

**MGA/Sections/Chapters/HKN:**

Program Co-Chair, Sections Congress 2017  
TA Program Coordinator, Sections Congress 2017, 2014, 2011  
Faculty Adviser, HKN UCSD Student Chapter 2016-present

**ENTREPRENEURSHIP:**

Vice Chair, IEEE/TAB AdHoc 2015-16  
Tutorial at PES Africa, Zambia, 2016

**WIE:**

Women in Tech Summit Keynote 2016  
Women in ECE Professional Panel 2016  
Founding Promoter SSCS Women in Circuits 2013

**SOCIETIES/COUNCILS:****SSCS:**

President 2012-13  
Vice President 2010-11  
Treasurer 2005-10  
CICC Representative to SSCS AdCom 2000-04  
Nominations Chair 2014-15  
Chair, Magazine Advisory Board, 2014-18  
Keynote/Plenary Talks: ASICON 2013, ASSCC 2011-12, ICE-ID 2013  
CICC Committee 1991-2004: Steering/Organizing, General Chair, Conference Chair,  
Program  
Chair, Exhibits Chair 1995-2004

**SSIT:**

Board of Governors, 2015-present; Chair, Turnaround AdHoc

**TEMS:**

Board of Governors 2008-11 (was a Council at the time)  
Fellow evaluation committee 2011-13

**EDS:**

Life Member